

FIG. 1

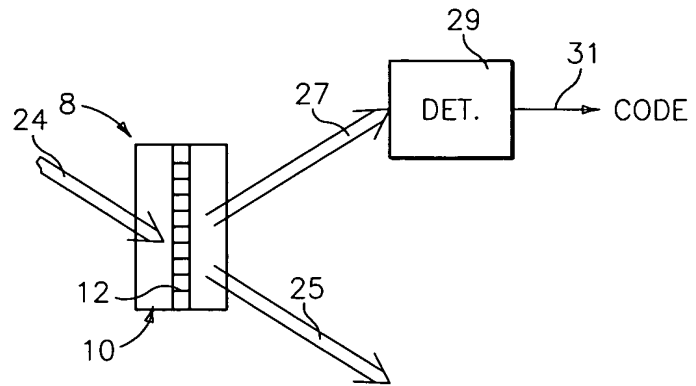


FIG. 2

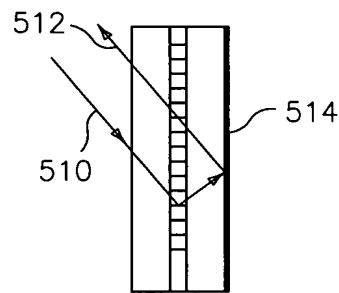
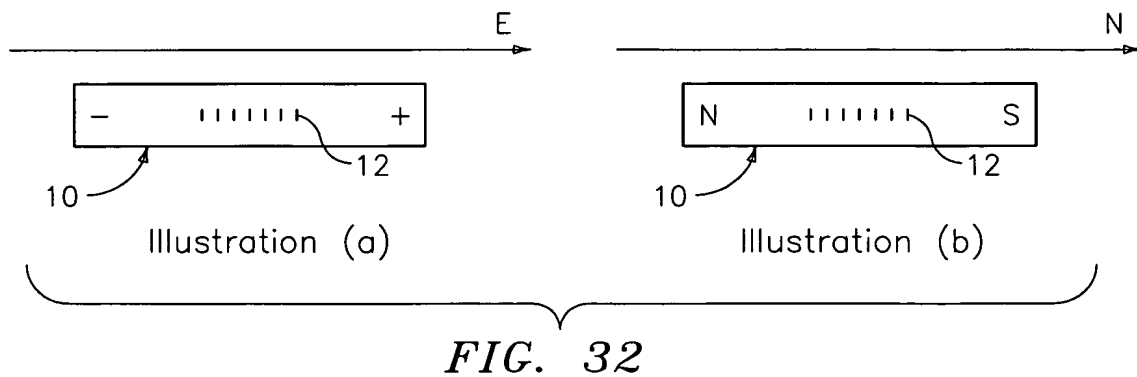


FIG. 31



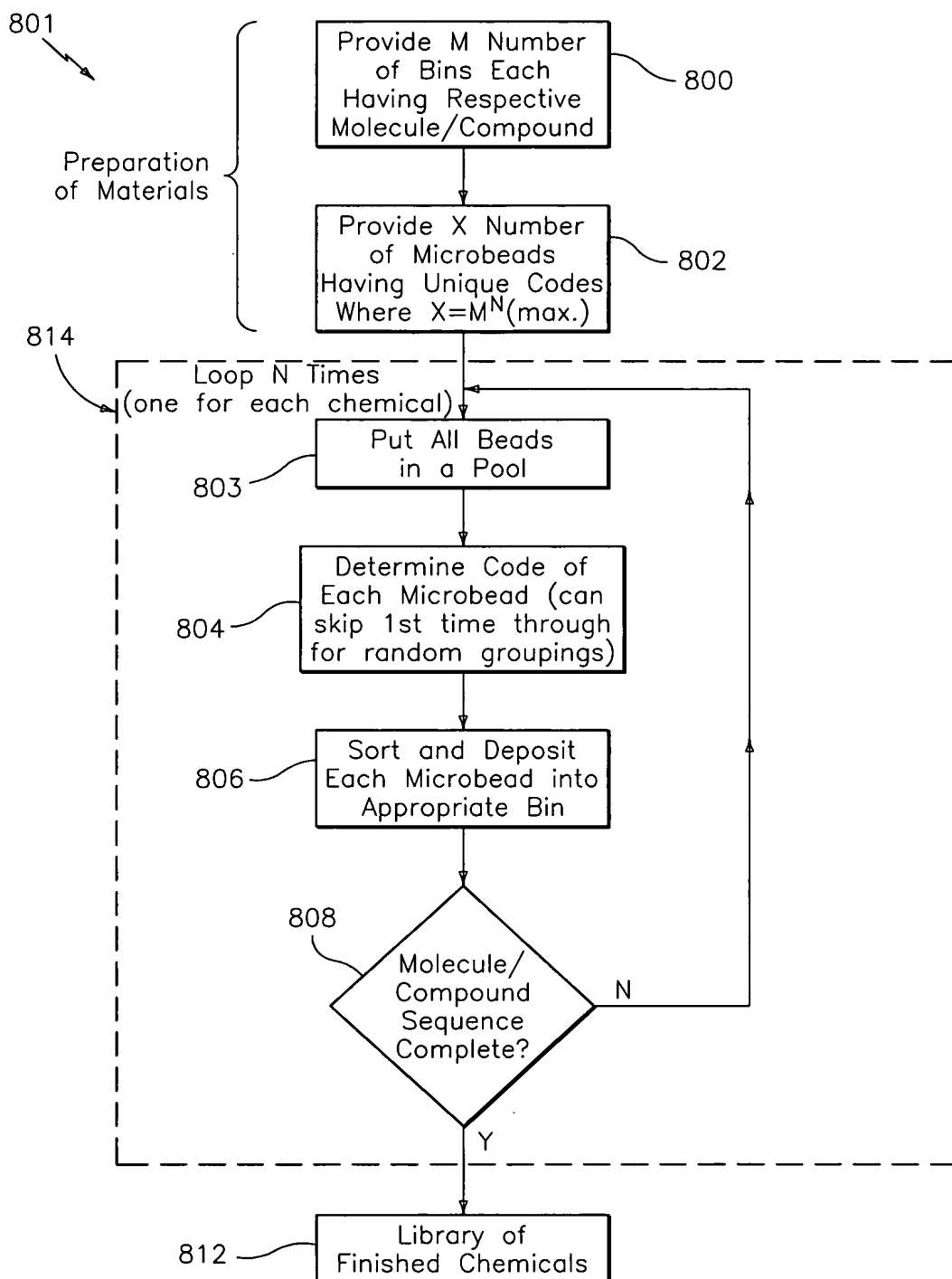


FIG. 3

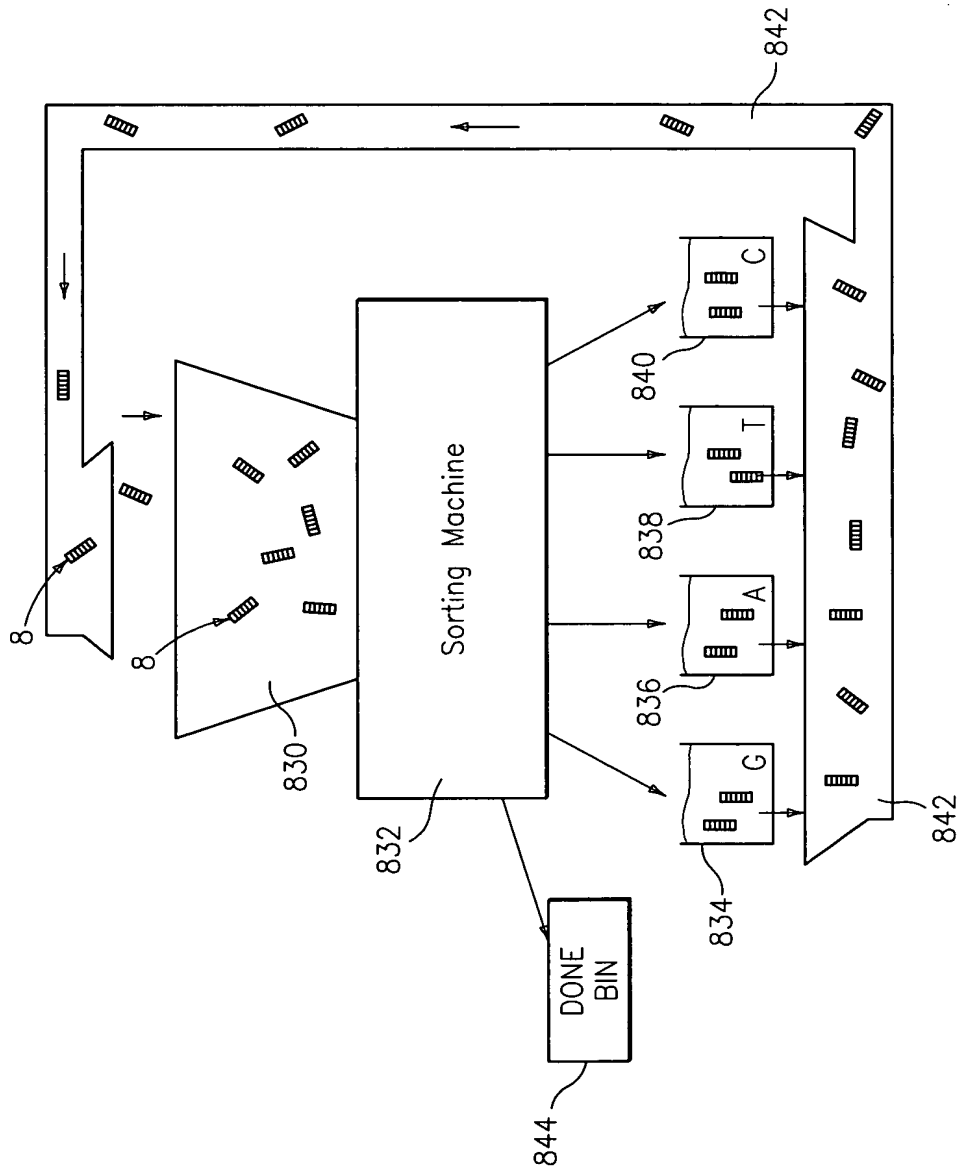


FIG. 4

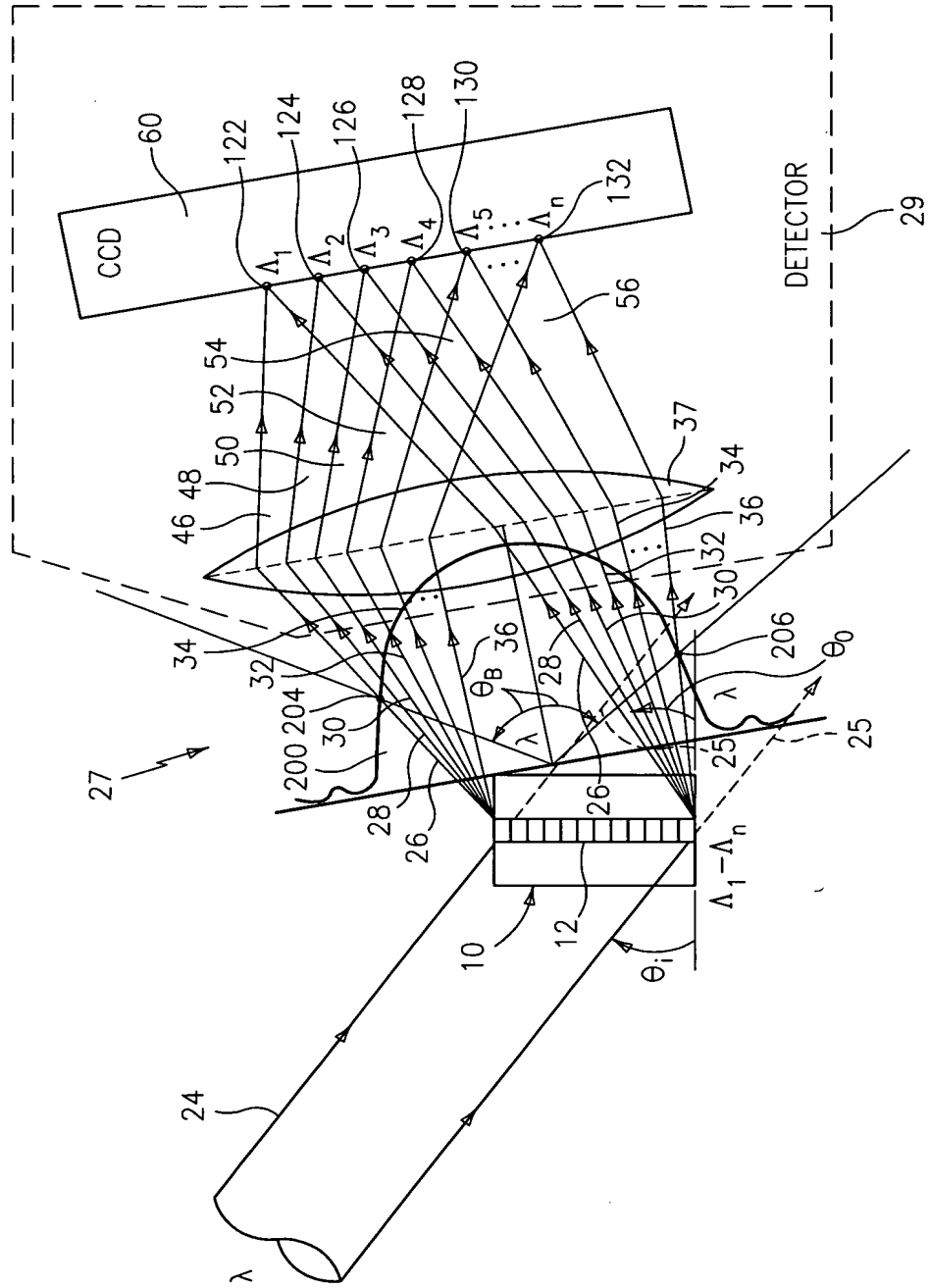


FIG. 5

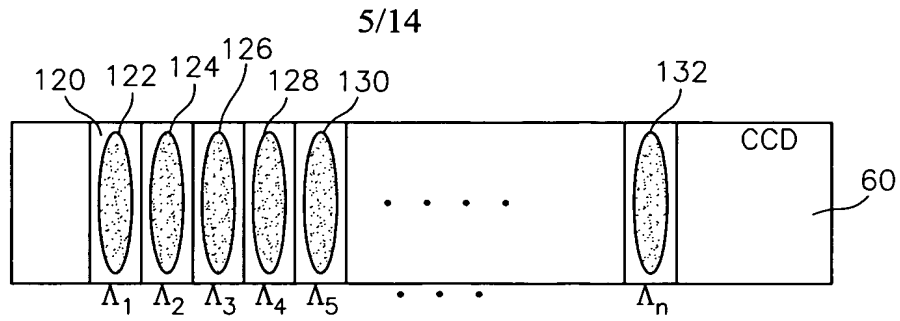


FIG. 6

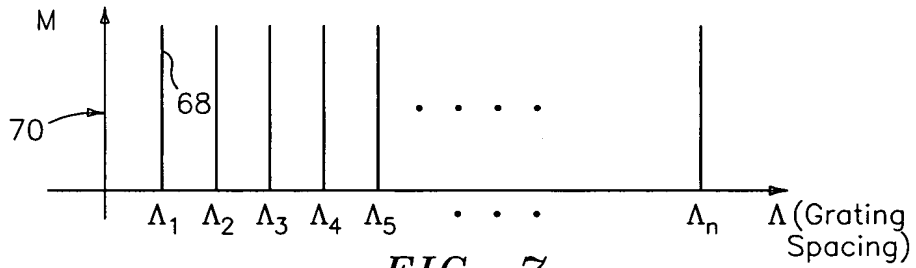
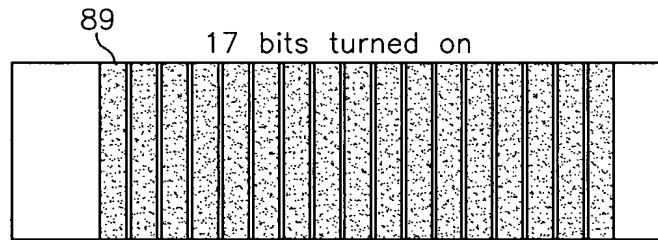
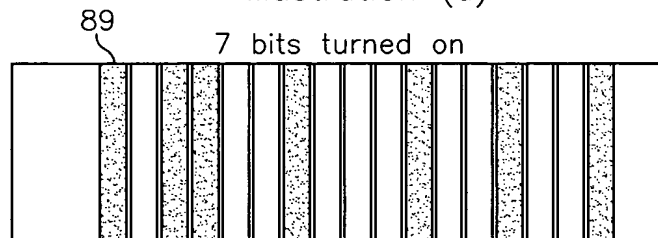


FIG. 7



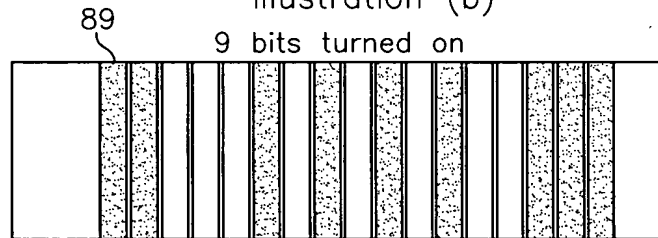
Bits: 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1

Illustration (a)



Bits: 1 0 1 1 0 0 1 0 0 0 1 0 0 1 0 0 1

Illustration (b)

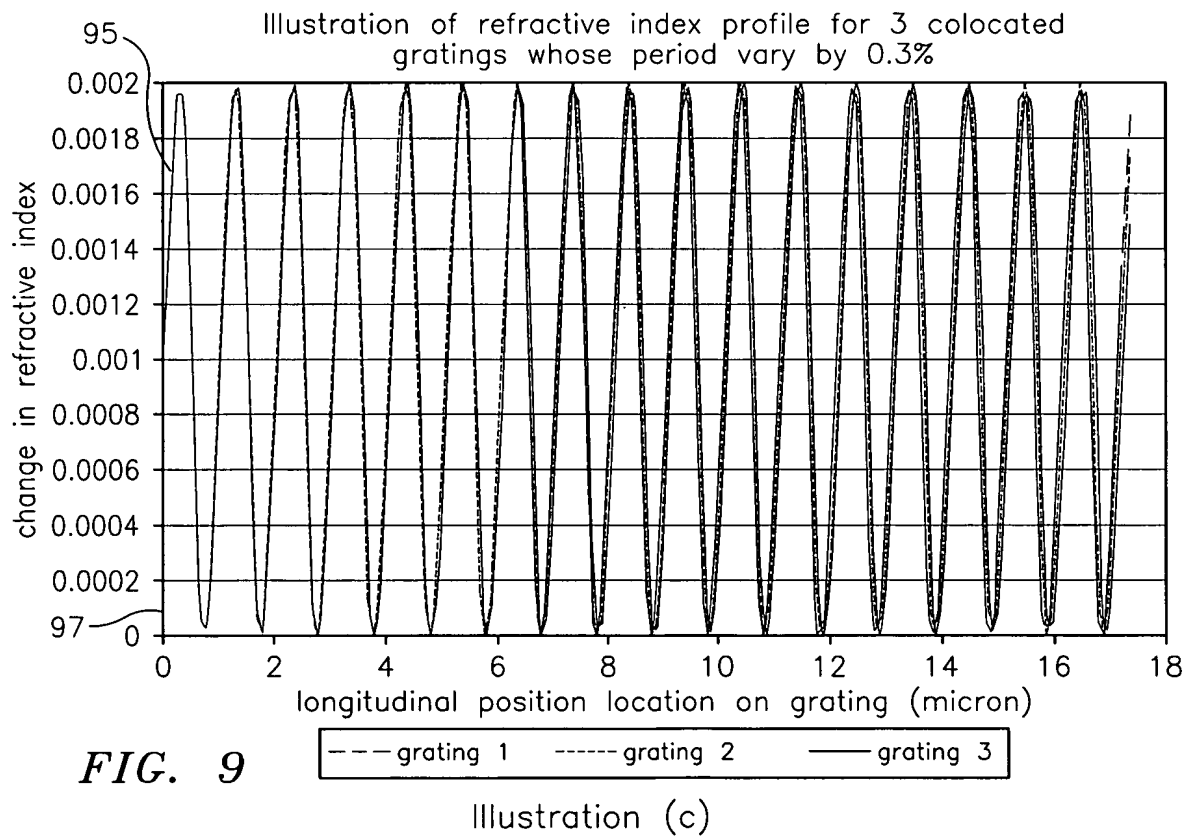
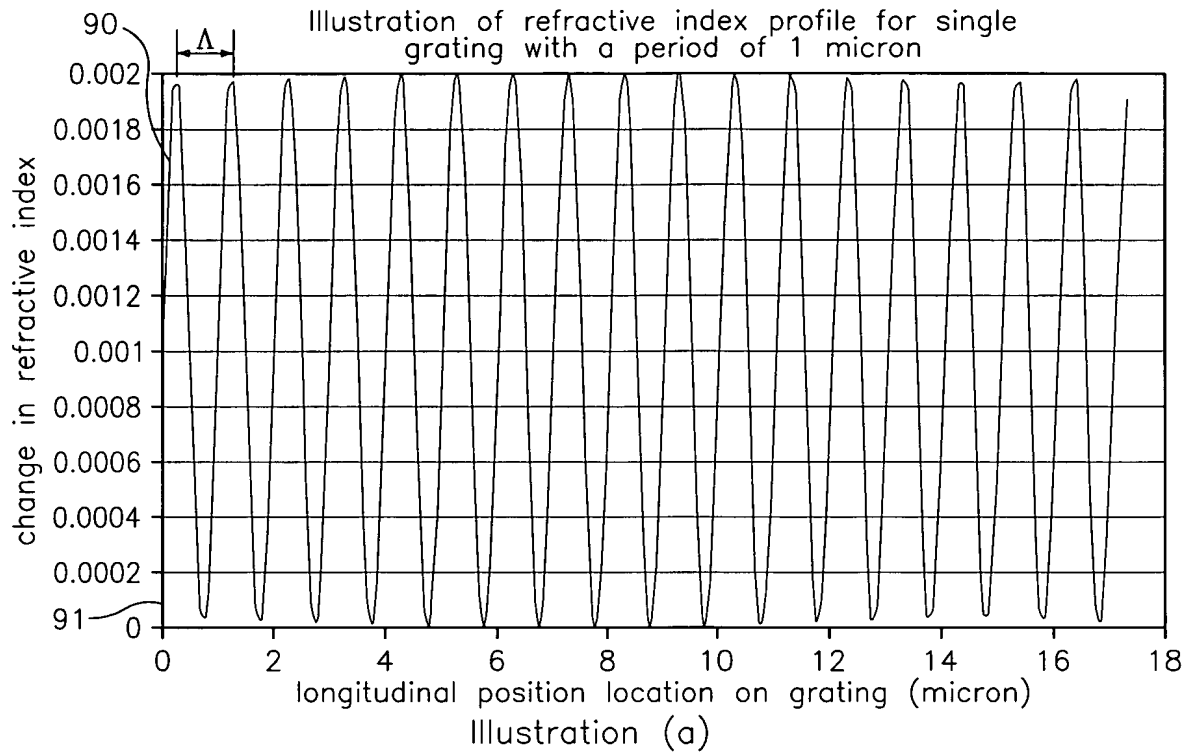


Bits: 1 1 0 0 0 1 0 1 0 1 0 1 0 0 1 1 1

Illustration (c)

FIG. 8

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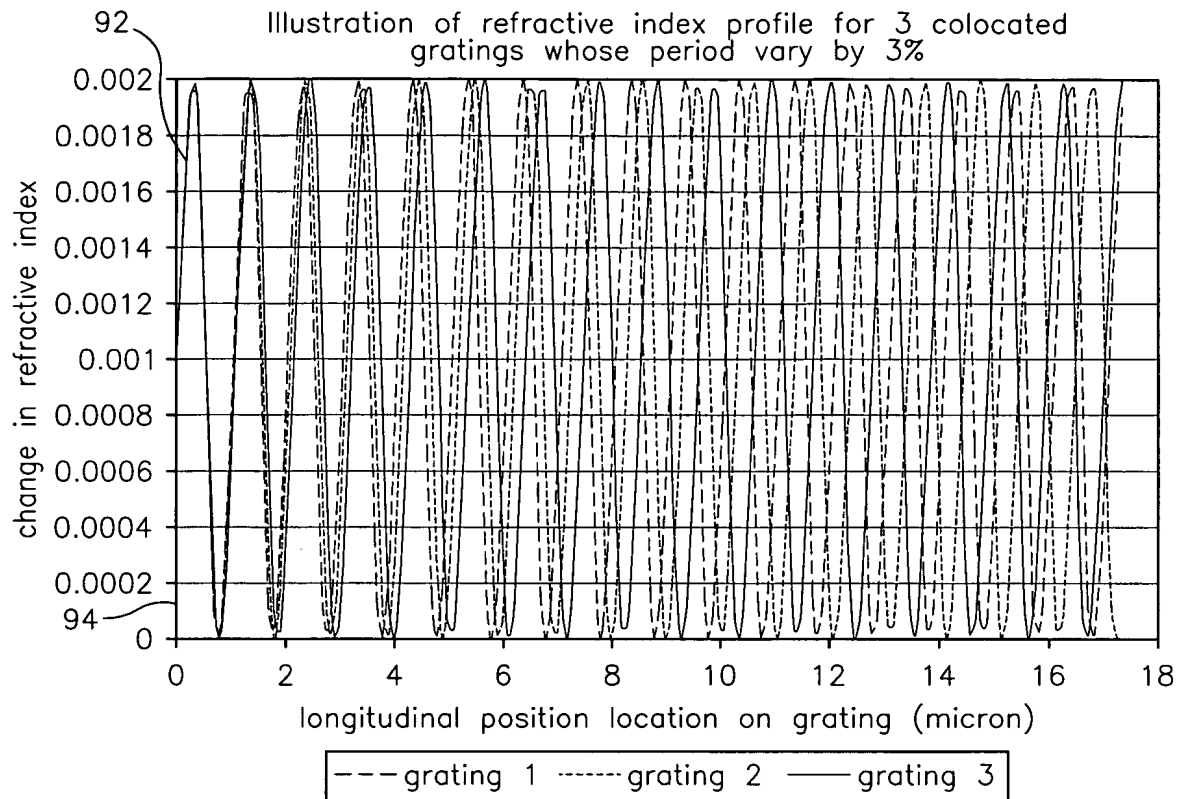


Illustration (b)

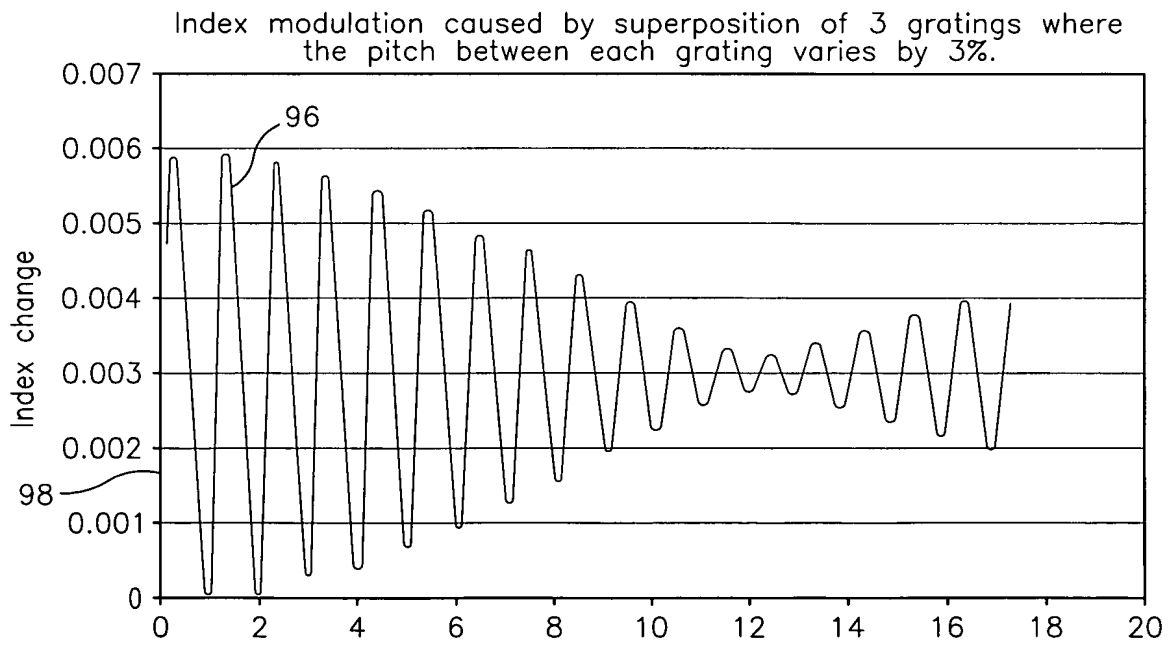
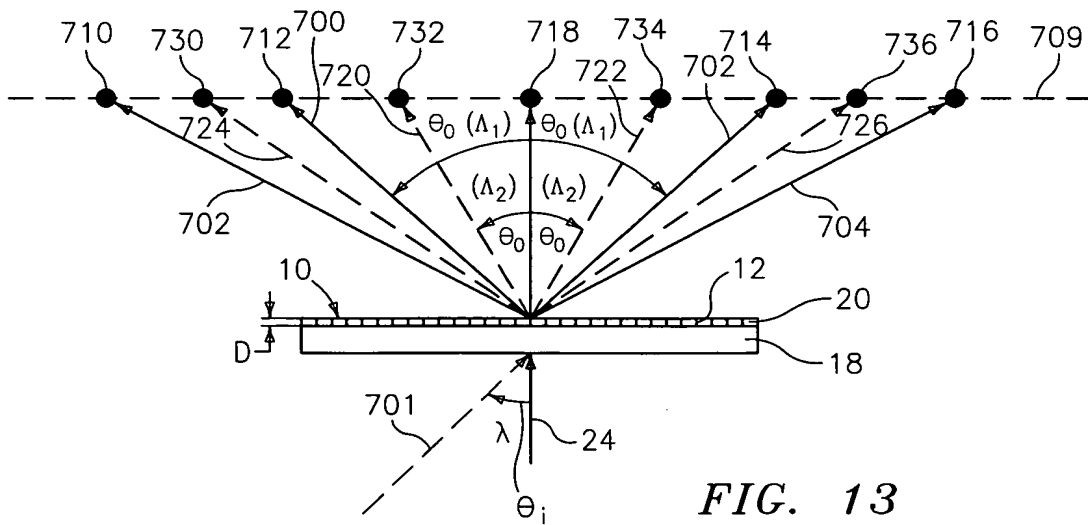
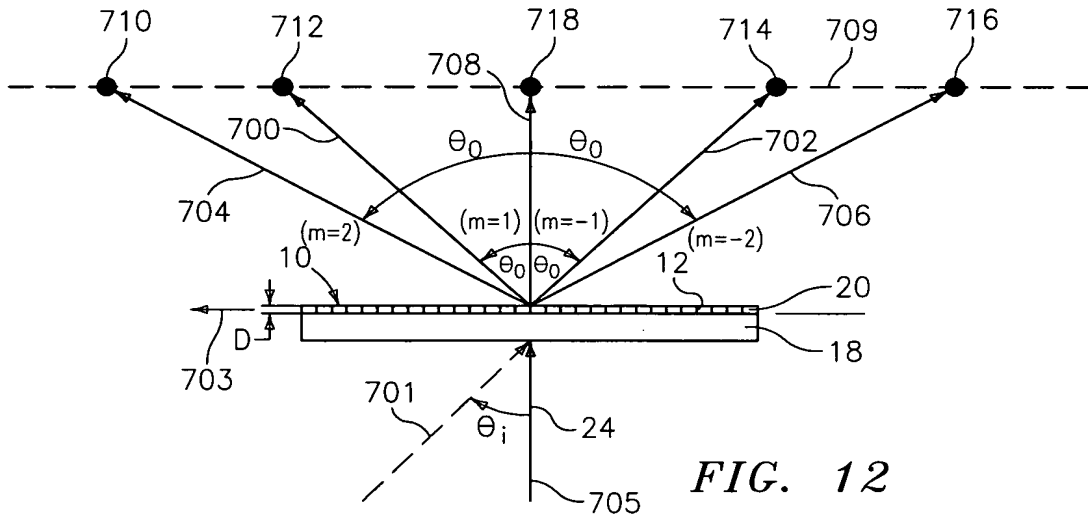
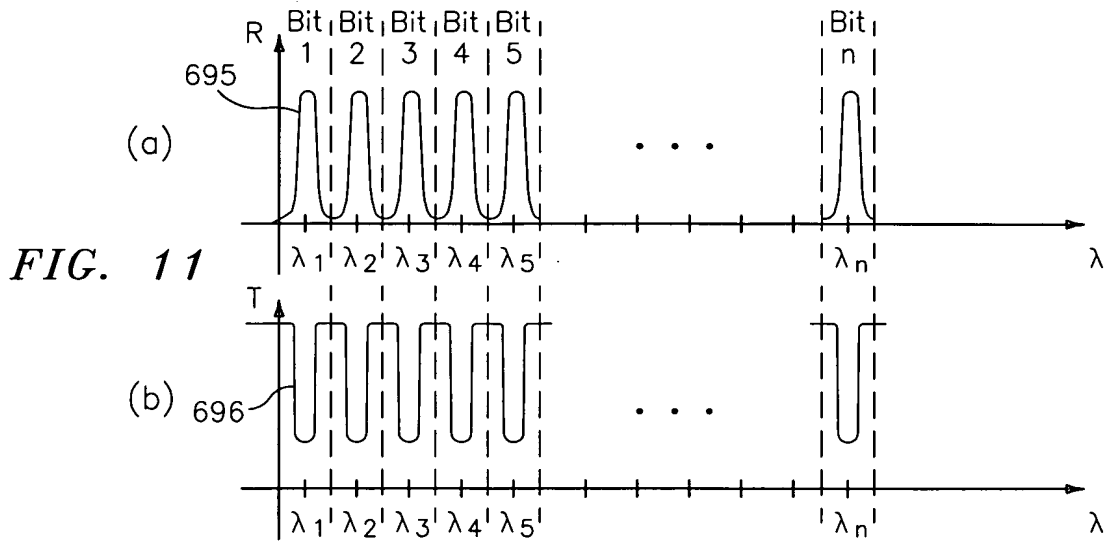


Illustration (d)

FIG. 9



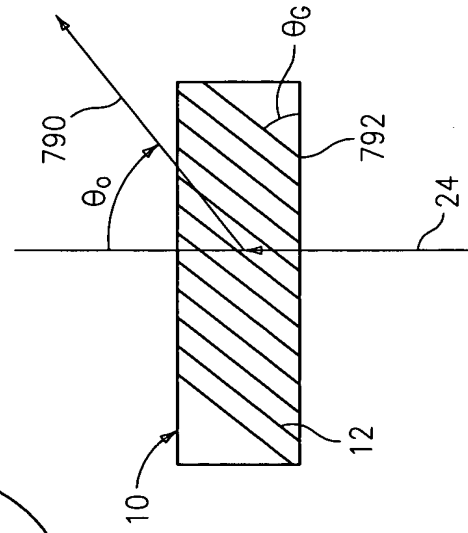
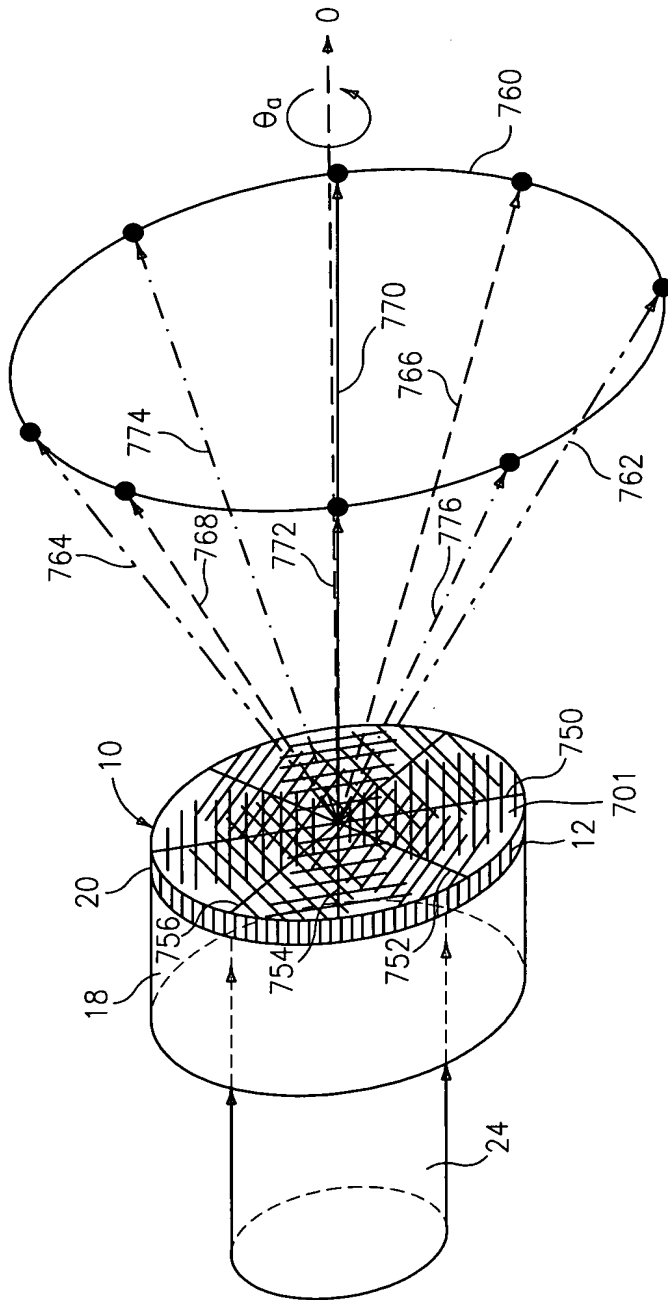


FIG. 15

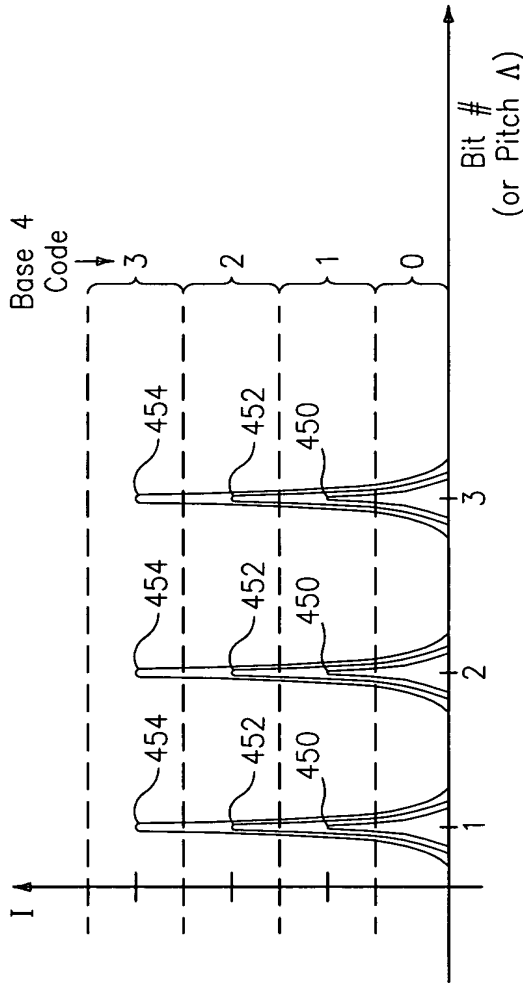


FIG. 16

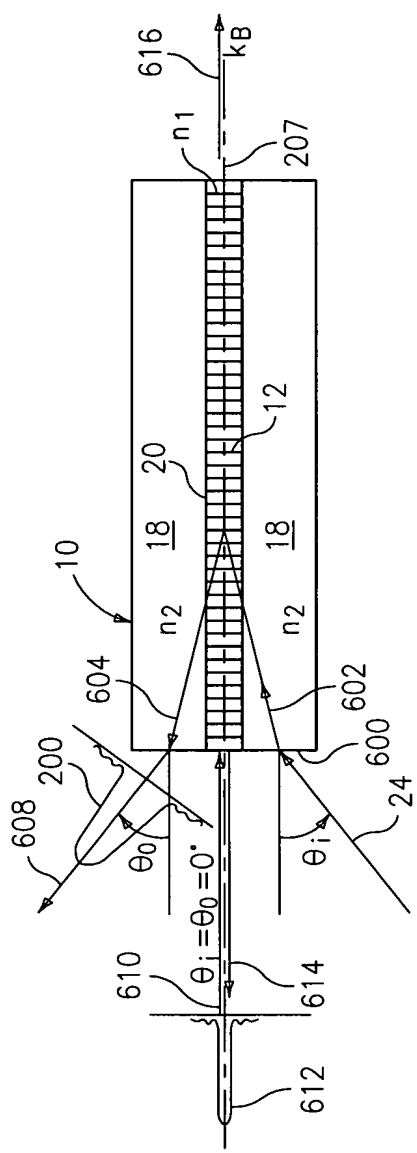


FIG. 17

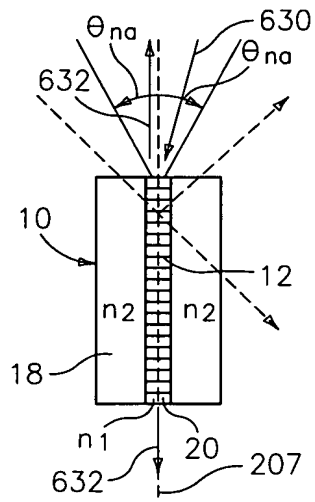


FIG. 18

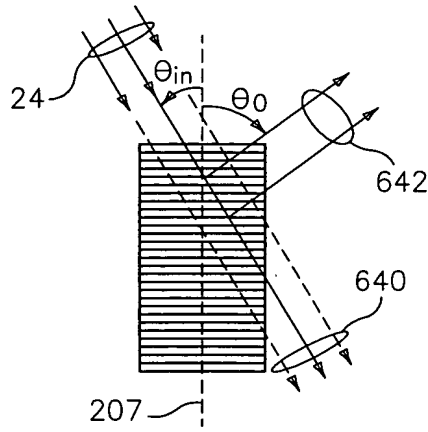
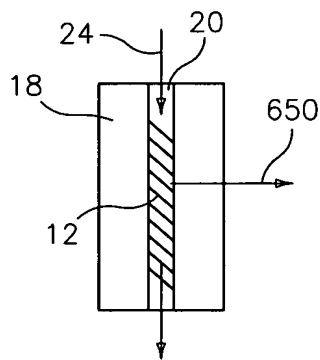
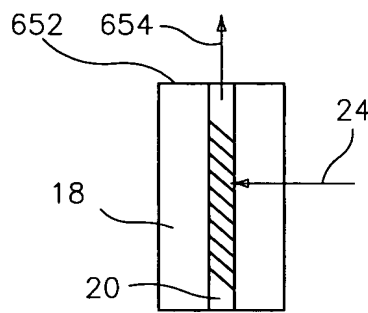


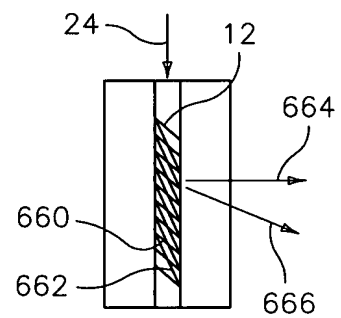
FIG. 19



Illustration(a)



Illustration(b)



Illustration(c)

FIG. 20

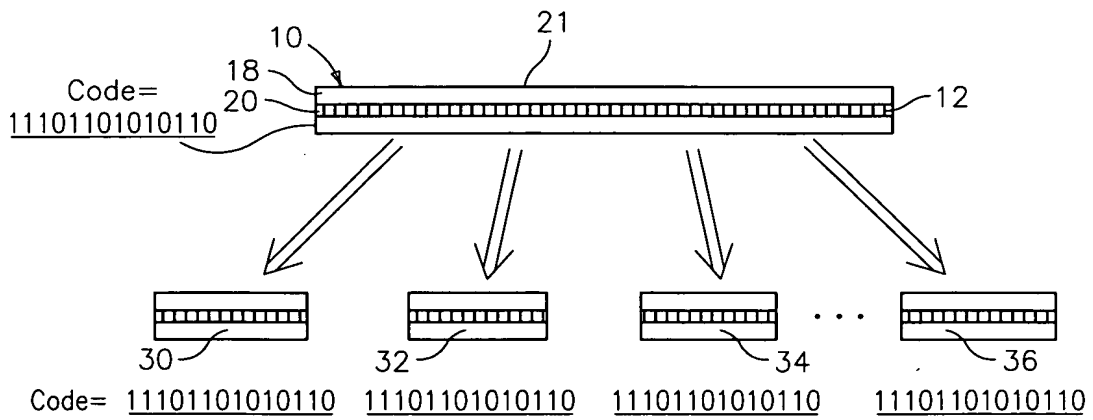


FIG. 22

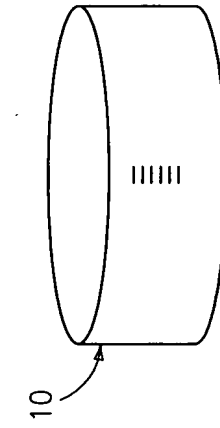
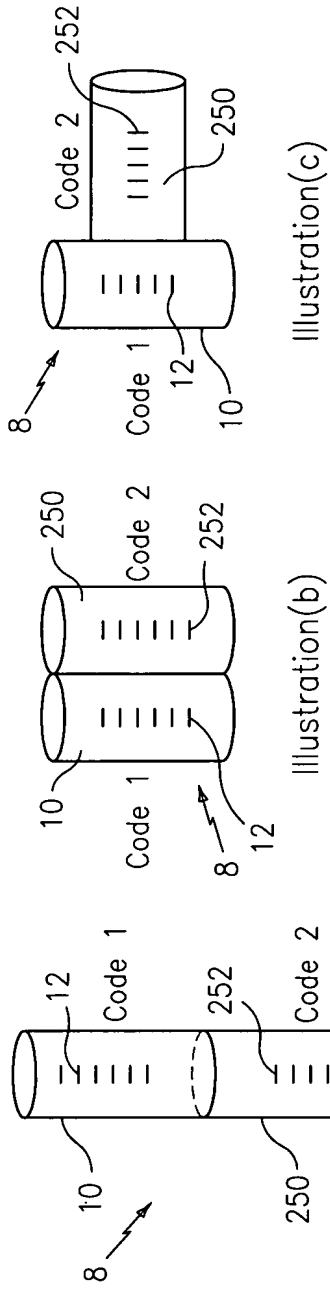


FIG. 24

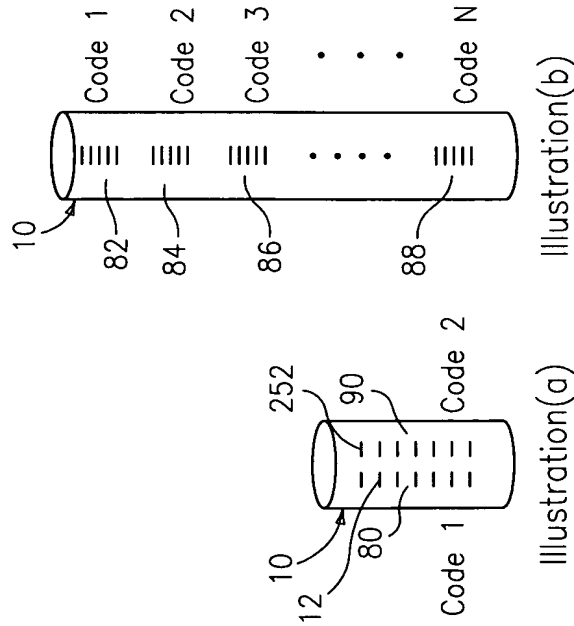


FIG. 26

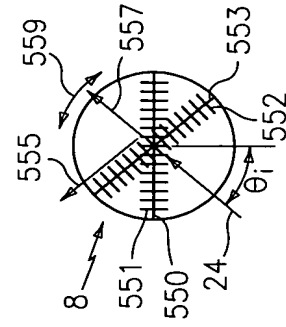


FIG. 21

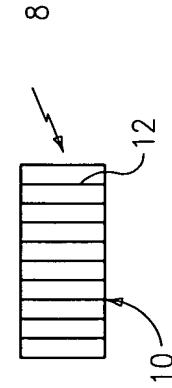


FIG. 23

FIG. 27

FIG. 25

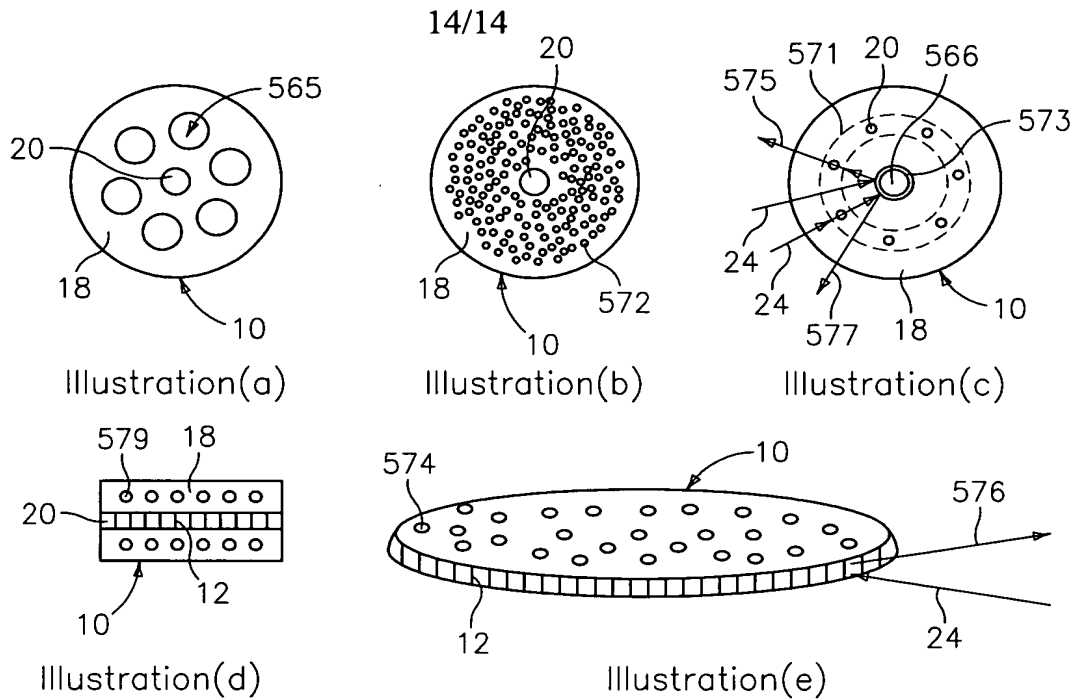


FIG. 28

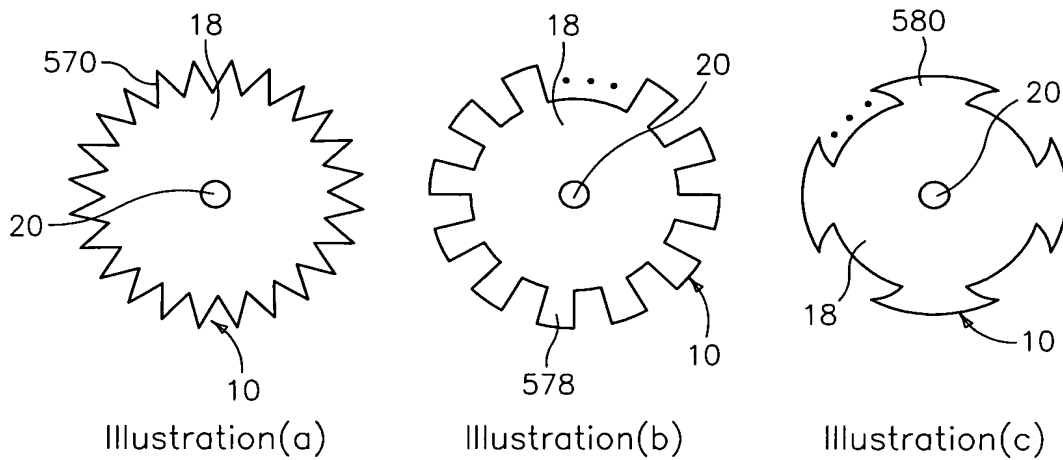


FIG. 29

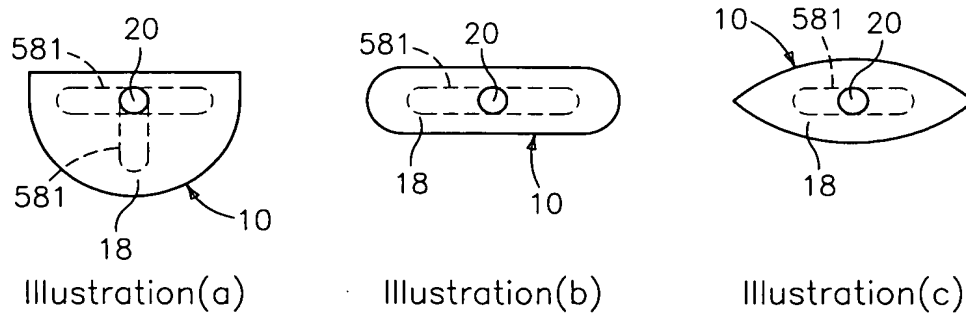


FIG. 30